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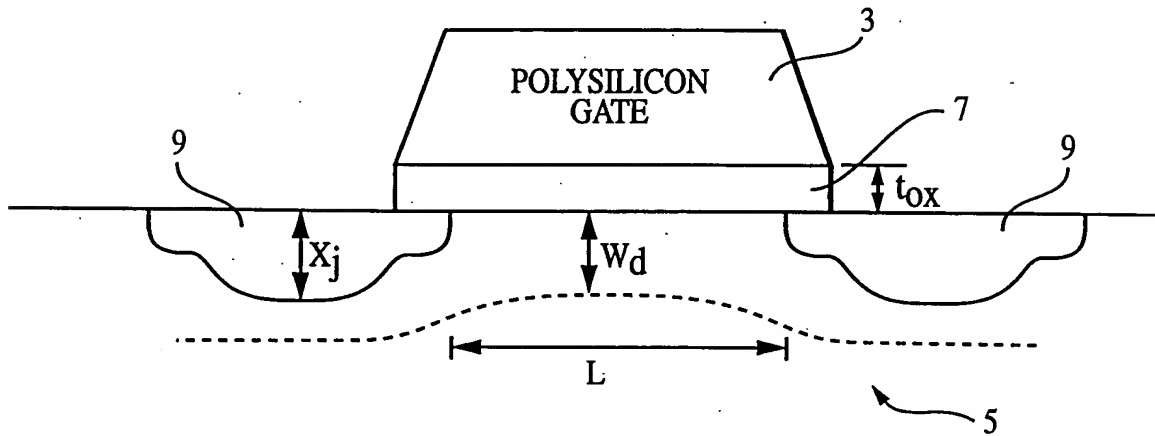
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App No.: Not Yet Assigned Docket No.: M4065.0381/P381-A
Inventor: Leonard Forbes et al.
Title: TECHNIQUE TO MITIGATE SHORT CHANNEL
EFFECTS WITH VERTICAL GATE TRANSISTOR WITH
DIFFERENT
GATE MATERIALS

FIG. 1a



ASPECT RATIO EQUATION

$$r = \frac{3L}{(w_d \cdot t_{ox} \cdot x_j)}$$

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FIG. 1b

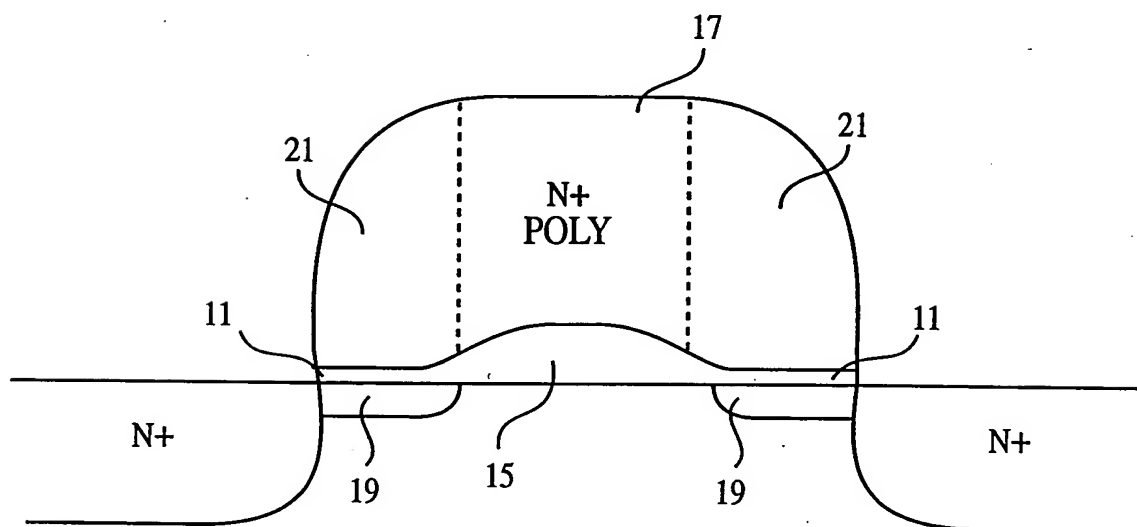


FIG. 2

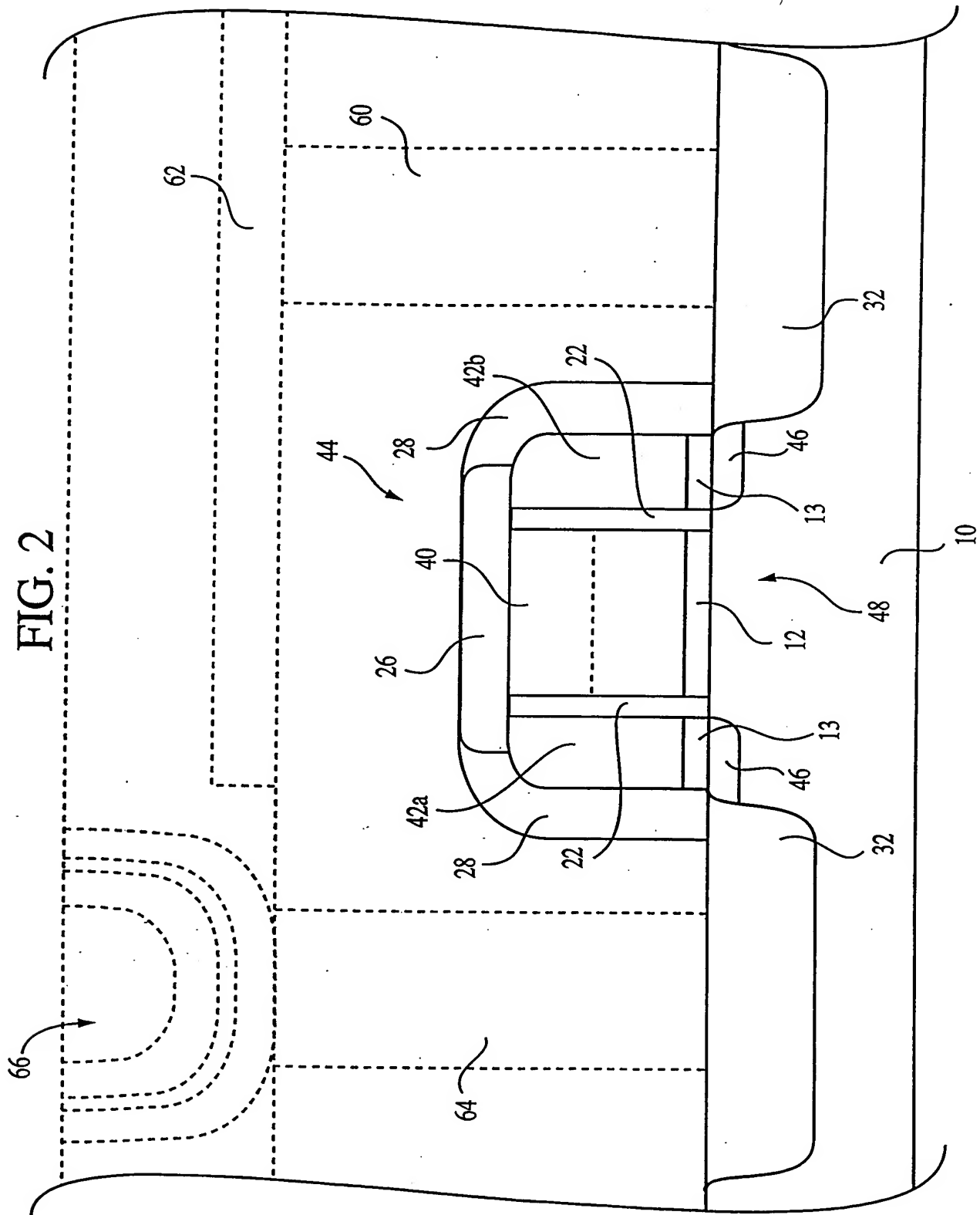


FIG. 2a

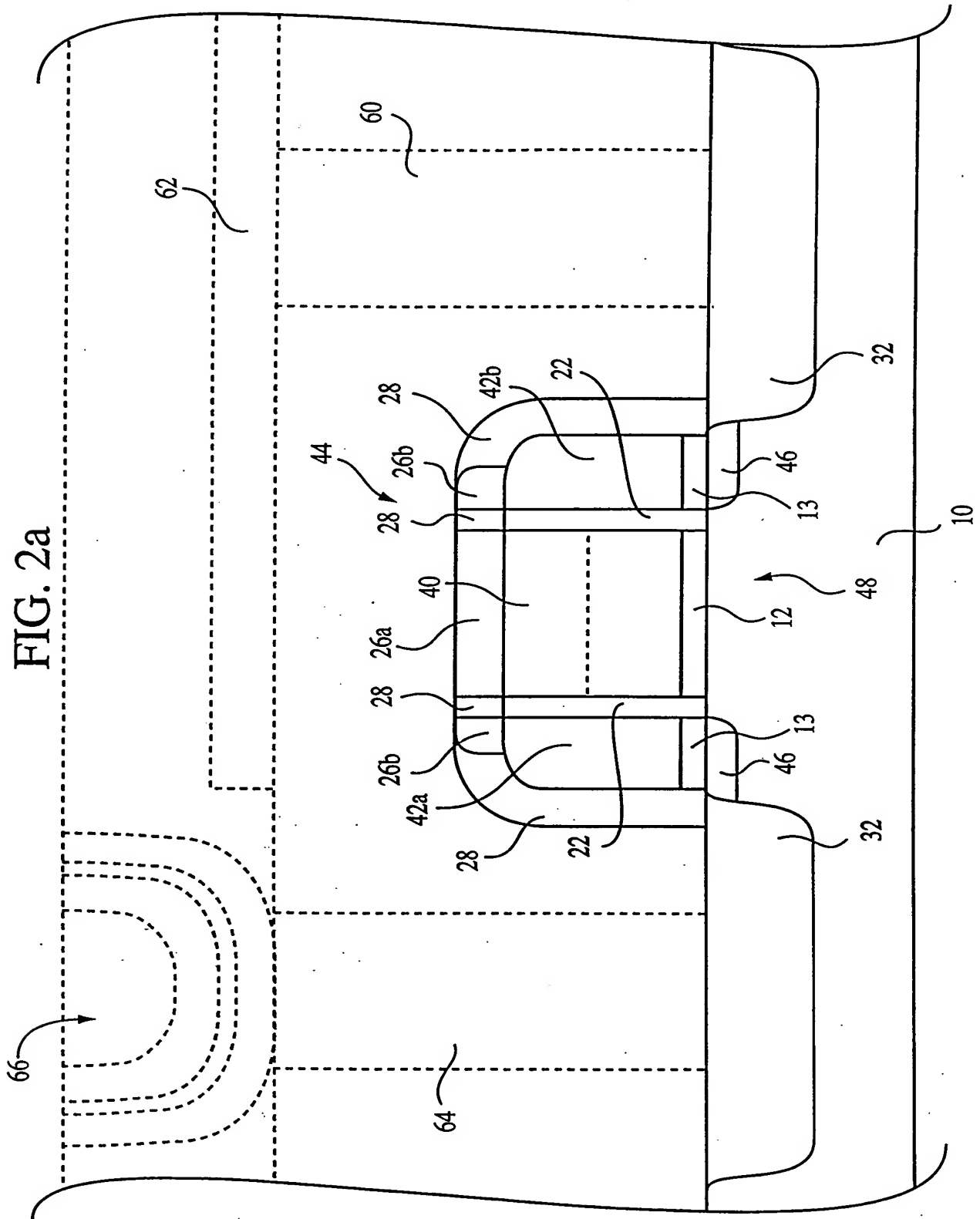


FIG. 3a

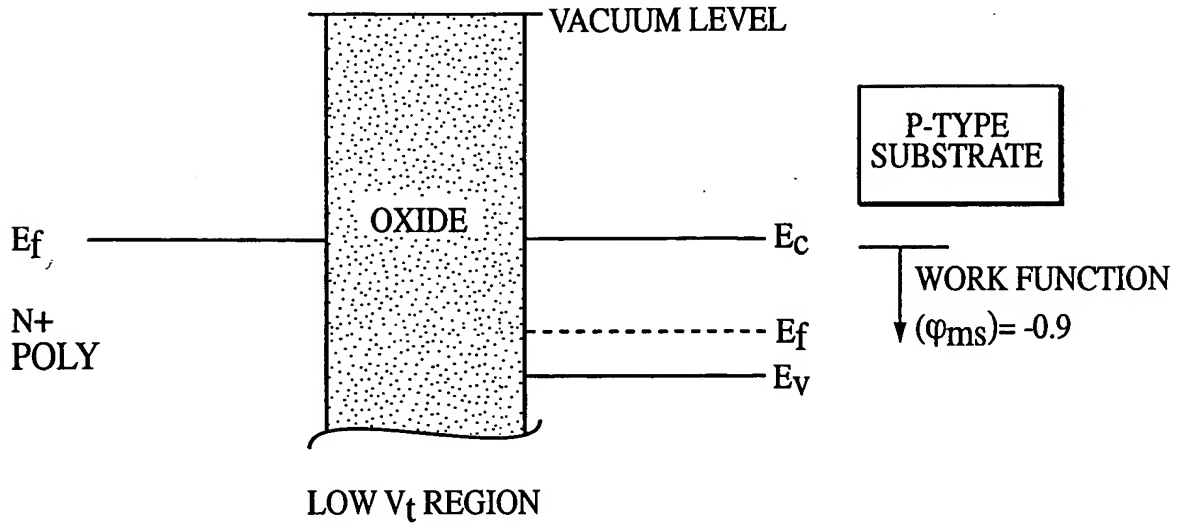


FIG. 3b

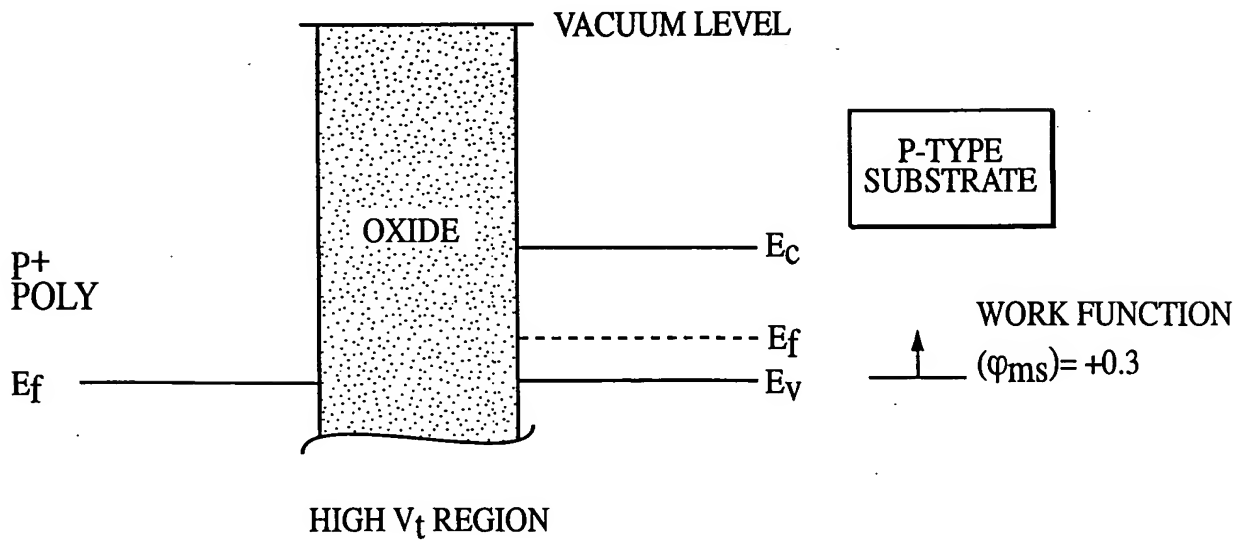


FIG. 4

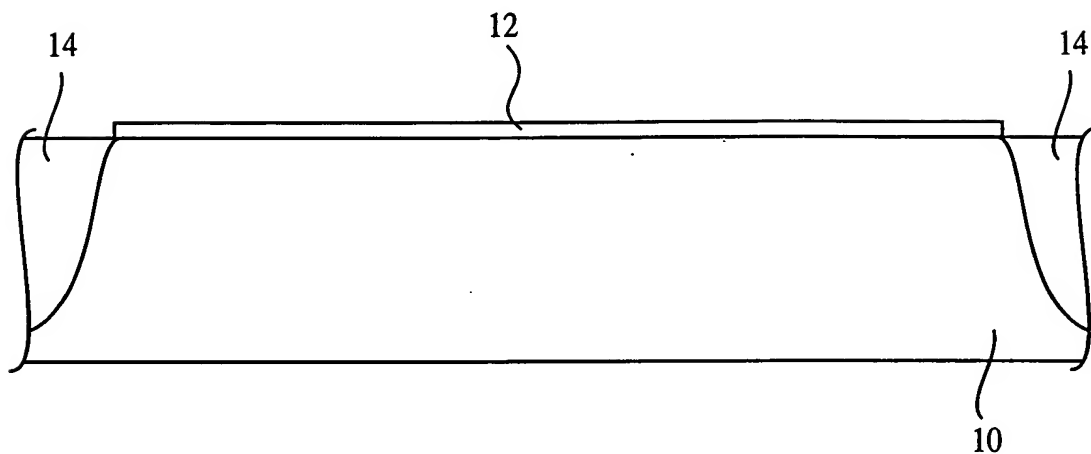


FIG. 5

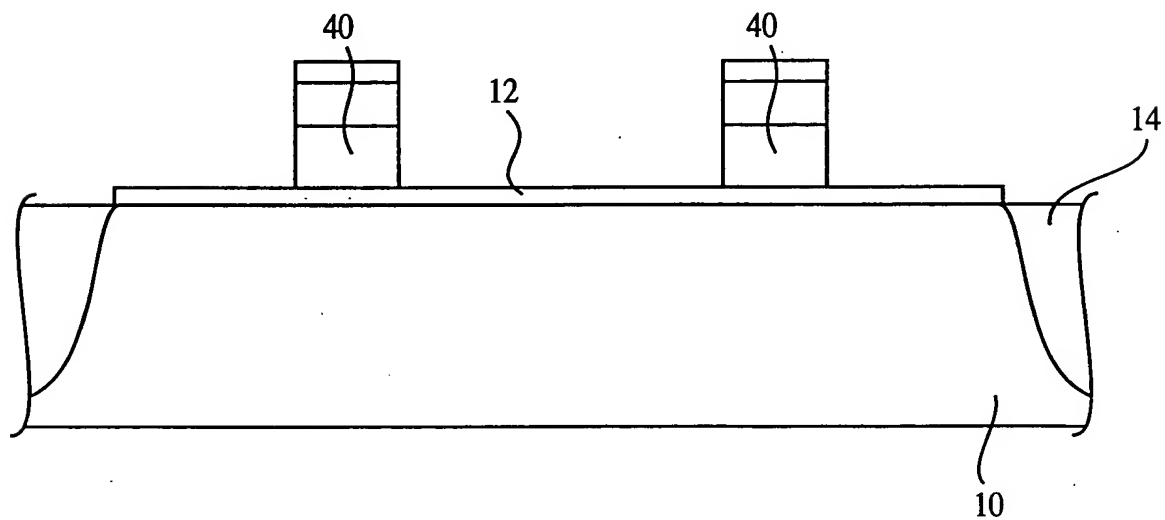


FIG. 6

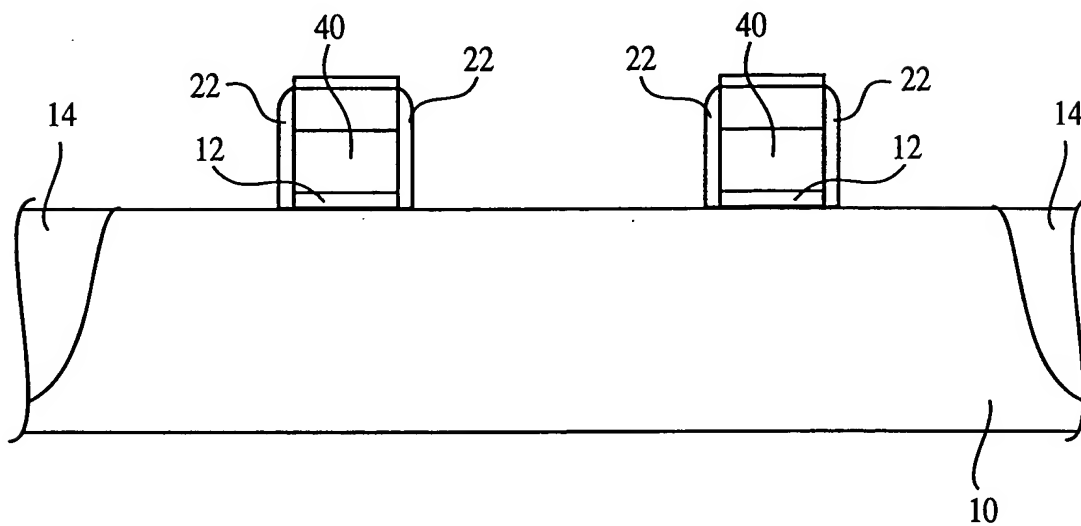


FIG. 7

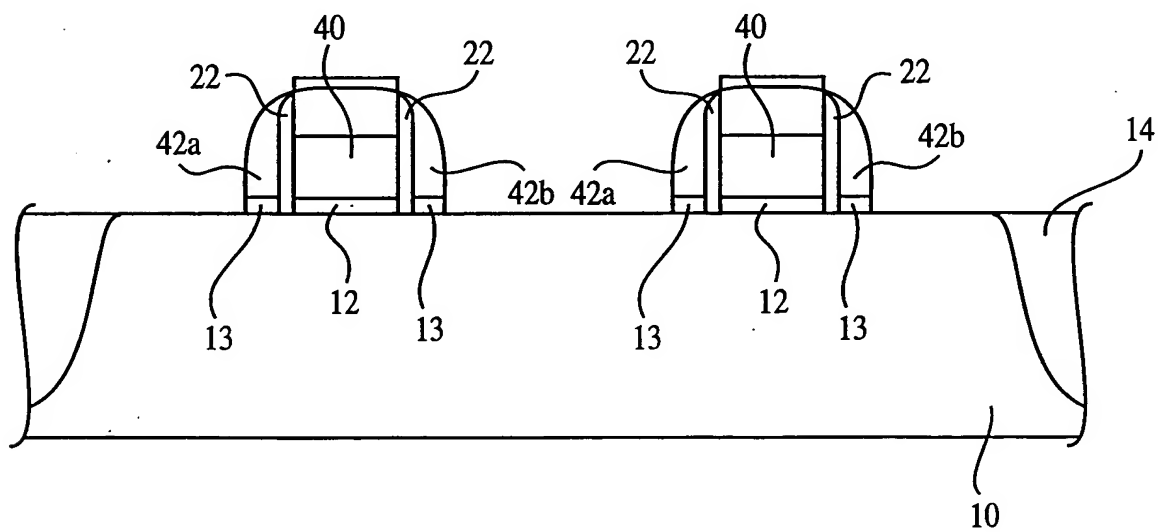


FIG. 8

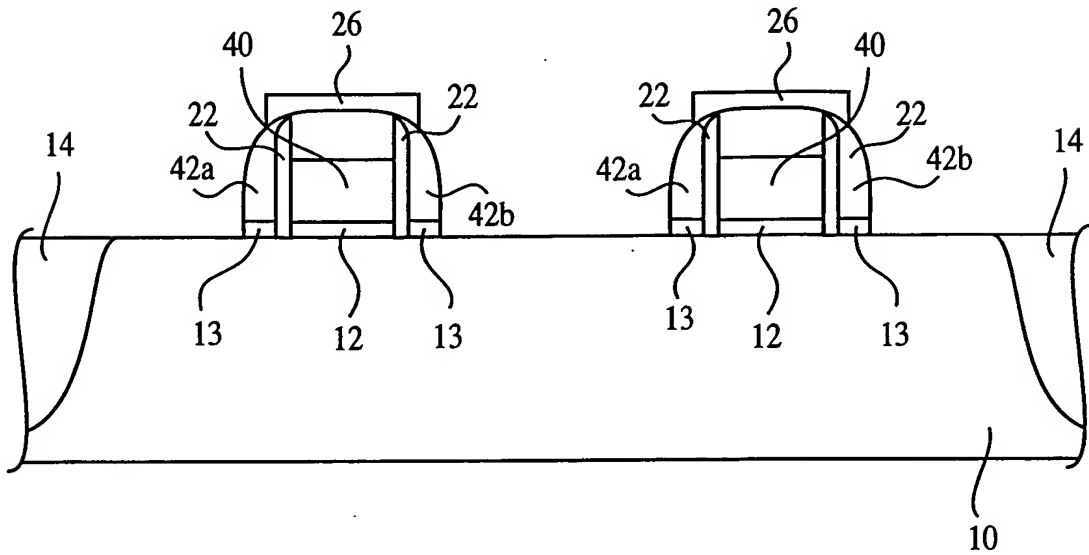


FIG. 9

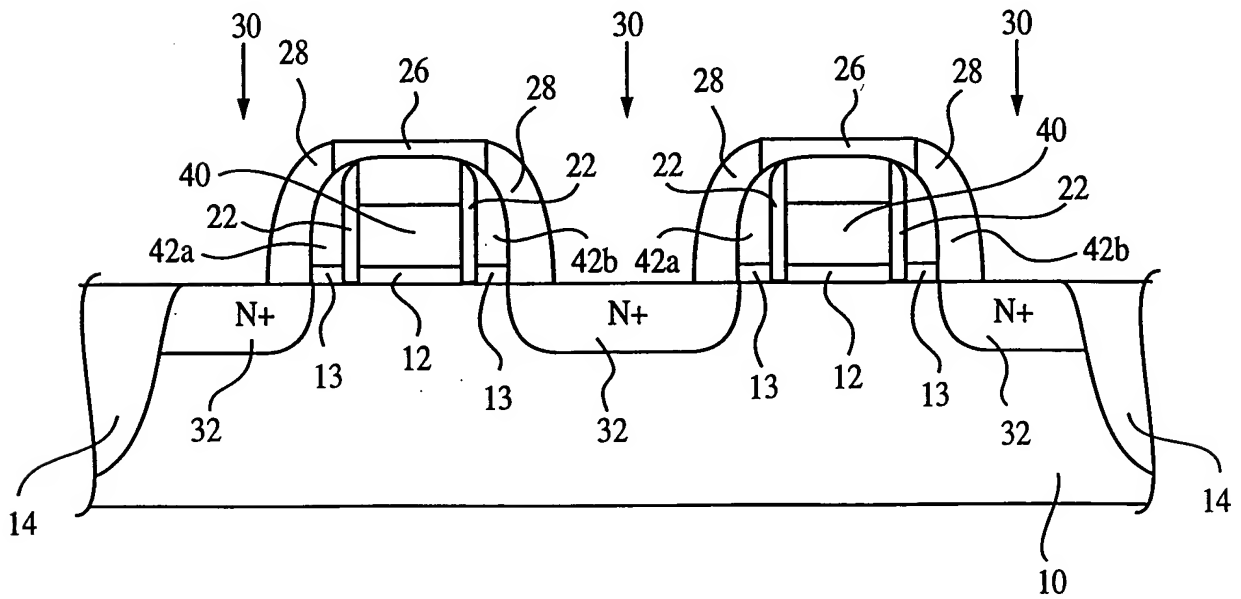


FIG. 10a

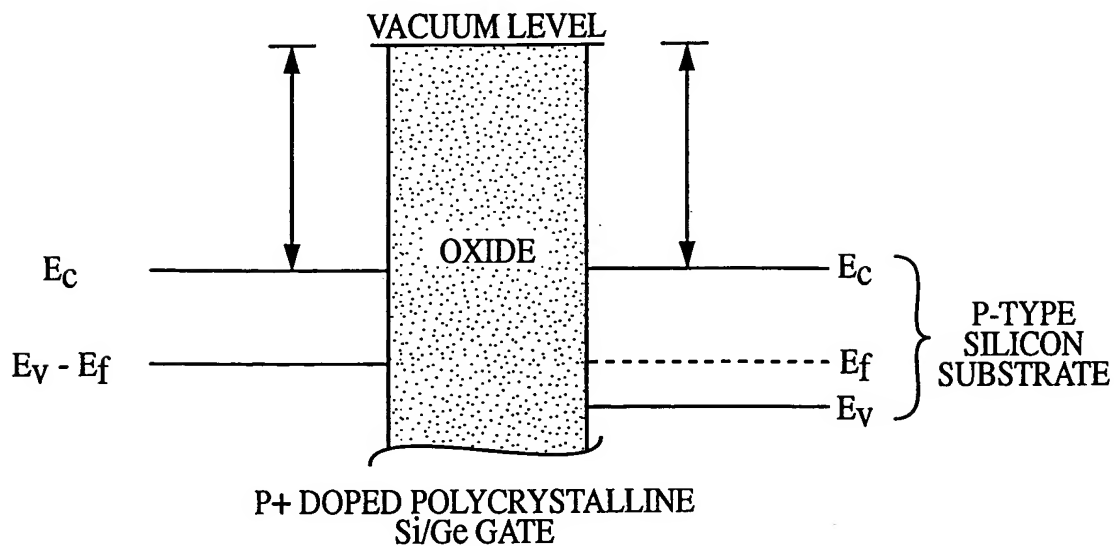


FIG. 10b

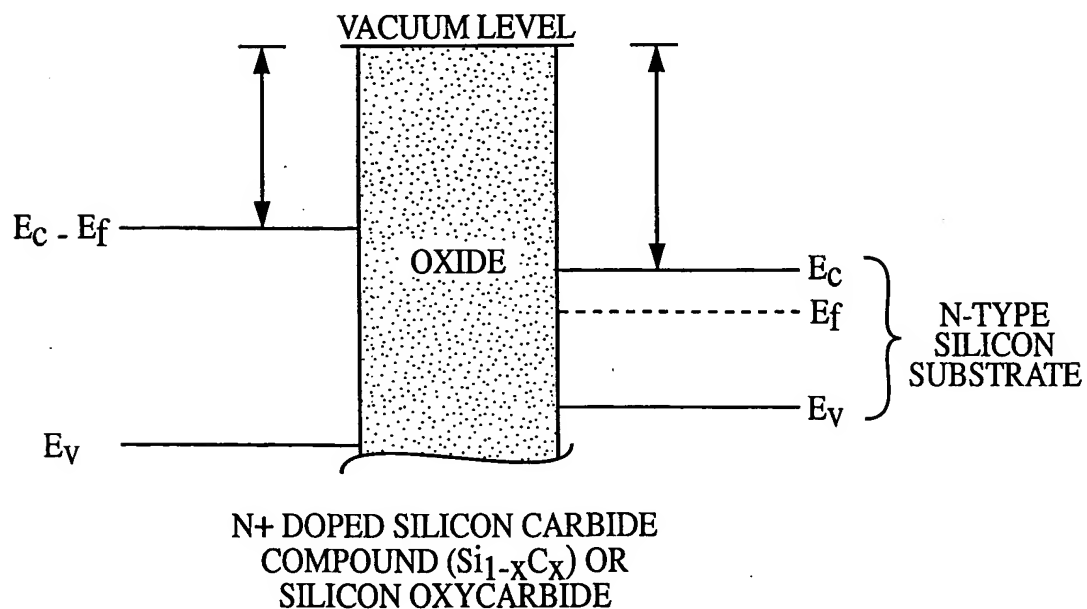


FIG. 11

